

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
**(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.  
50033-00006

In Re Application Of: **VAN ZEGHBROECK et al**

Serial No.  
10/630,238

Filing Date  
July 30, 2003

Examiner  
Not Yet Assigned

Group Art Unit  
1765

Title:

**"WIDE BANDGAP SEMICONDUCTOR DEVICE CONSTRUCTION"**



Address to:  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
- ☐ the statement specified in 37 CFR 1.97(e);
- OR**
- ☐ the fee set forth in 37 CFR 1.17(p).

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"WIDE BANDGAP SEMICONDUCTOR DEVICE CONSTRUCTION"



**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account **50-1419** as described below.
- ☐ Charge the amount of \_\_\_\_\_
- ☐ Credit any overpayment.
- ☒ Charge any additional fee required.

**Certificate of Transmission by Facsimile\***

I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Ft \_\_\_\_\_)

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Signature

Typed or Printed Name of Person Signing Certificate

**Certificate of Mailing by First Class Mail**

I certify that this document and fee is being deposited on **November 5, 2003** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature of Person Mailing Correspondence

Bobbye D. Simon

Typed or Printed Name of Person Mailing Certificate

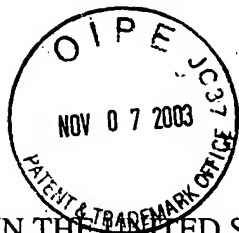
**\*This certificate may only be used if paying by deposit account.**

  
Signature

Dated: November 5, 2003

Travis C. Stephenson, Esq.  
Registration No. 45,132  
MARSH FISCHMANN & BREYFOGLE LLP  
3151 South Vaughn Way, Suite 411  
Aurora, Colorado 80014  
(720)562-5500

CC:



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

VAN ZEGHBROECK et al

Serial No.:

Filed: July 30, 2003

Confirmation No.:

Atty. File No.: 50033-00006

For: "WIDE BANDGAP  
SEMICONDUCTOR DEVICE  
CONSTRUCTION"


Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to Applicant's duty of disclosure under 37 CFR § 1.56 and 37 CFR §§ 1.97-1.98, Applicant hereby provides a copy of each of the documents identified on the enclosed PTO Form 1449, although Applicant does not admit that any of such documents, alone or in any combination, is considered to be material to patentability as defined in 37 CFR § 1.56(b). Moreover, the inclusion of these documents is not to be construed as an admission by Applicant that each such document is prior art as to the above-identified application.

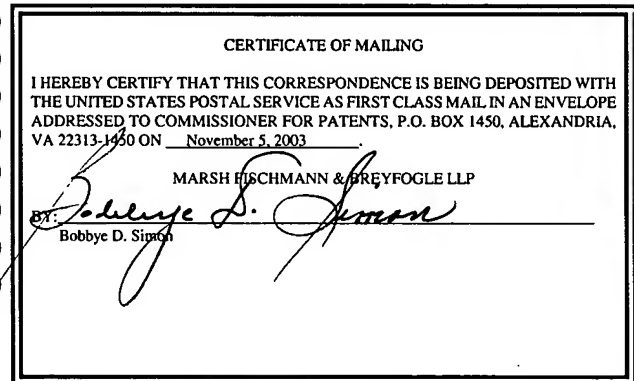
Respectfully submitted,

MARSH FISCHMANN & BREYFOGLE LLP

By:   
Travis C. Stephenson, Esq.  
Registration No. 45,132  
3151 South Vaughn Way, Suite 411  
Aurora, Colorado 80014  
(720)562-5500

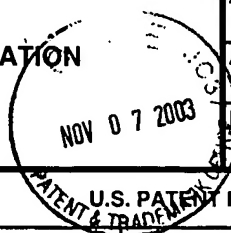
Date: November 5, 2003

) Group Art Unit: 1765  
)  
) Examiner: Not Yet Assigned  
)  
) INFORMATION DISCLOSURE STATEMENT  
)



# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



ATTY DOCKET NO.

50033-00006

SERIAL NO.

10/630,238

APPLICANT(S)

Van Zeghbroeck, et al

FILING DATE

July 30, 2003

GROUP

1765

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	a.	H1637	03/04/1997	Offord et al	437	173	
	b.	4,945,394	07/31/1990	Palmour et al	357	34	
	c.	5,266,503	11/30/1993	Wang et al	437	24	
	d.	6,180,958	01/30/2001	Cooper, Jr.	257	77	
	e.	6,181,200	01/30/2001	Titizian et al	330	66	
	f.	6,303,475	10/16/2001	Suvorov et al	438	519	
	g.	6,313,482	11/06/2001	Baliga	257	77	
	h.	6,323,506	11/27/2001	Alok	257	77	
	i.	6,329,675	12/11/2001	Singh et al	257	77	
	j.	6,331,931	12/18/2001	Titizian et al	361	306.3	
	k.	6,218,254	04/17/2001	Singh et al	438	364	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		l.	Article entitled "Silicon Carbide Bipolar Transistor" by W. V. Munch and P. Hoeck published in <i>Solid State Electronics</i> , 1978, Vol. 21, pp. 479-480, Pergamon Press, Great Britain
		m.	Excerpt from textbook <i>Volume V Introduction to Microelectronic Fabrication</i> , pp. 71-73, author Richard C. Jaeger, Auburn University, published by Addison-Wesley Publishing Company, reprinted with corrections May, 1993.

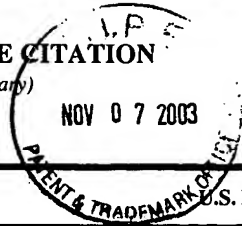
EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

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Applicant(s)

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	o.	5,914,611	06/22/1999	Cheng	324	719	
	o.	6,228,665	05/08/2001	Griffith et al	438	14	

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	p.	U. S. Patent Application Serial No. 10/360,662, filed on February 7, 2003, entitled "Method of Fabricating Self-Aligned Silicon Carbide Semiconductor Devices", and further identified as Attorney Docket No. 50033-00005
	q..	U. S. Patent Application Serial No. 10/339,040, filed on January 9, 2003, entitled "Silicon Carbide Semiconductor Devices with a Regrown Contact Layer", and further identified as Attorney Docket No. 50033-00004.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.